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PhD in Electrical Engineering from Columbia University, 1993. BA in Physics, Queens College, 1986.

Prof. Shor has published more than 60 papers in refereed Journals and Conference Proceedings in the areas of Analog Circuit Design and Device Physics. He holds over 40 issued patents and several pending patents. He is presently an Associate Professor of Electrical Engineering at Bar Ilan University, Ramat Gan, Israel. From 2004-2015, he was at Intel Israel, as a Principal Engineer, and head of the Analog Team at Intel Yakum. Between 1999-2004, he worked at Saifun Semiconductor, Netanya, Israel, as a Staff Engineer where he established the analog activities for Flash and EEPROM NROM memories. From 1994-1999, he was a Senior Analog Designer at Motorola Semiconductor, Israel, in the DSP Division. From 1988-1994, he was a Senior Research Scientist at Kulite Semiconductor, NJ, USA, where he developed processes and devices for Silicon Carbide and Diamond Microsensors. His present interests include Analog circuits, Switching and Linear Voltage Regulators, thermal Sensors, PLLs and IO circuits, Microprocessors and Security. He was a member of the ISSCC Technical Program Committee (TPC) from 2014-2018, is a member of the TPC of ESSCIRC, and is an Associate Editor for IEEE Sensors Journal. He has been a guest Associate Editor of JSSC.

For more information, see his personal website at <http://www.eng.biu.ac.il/shorjos/>